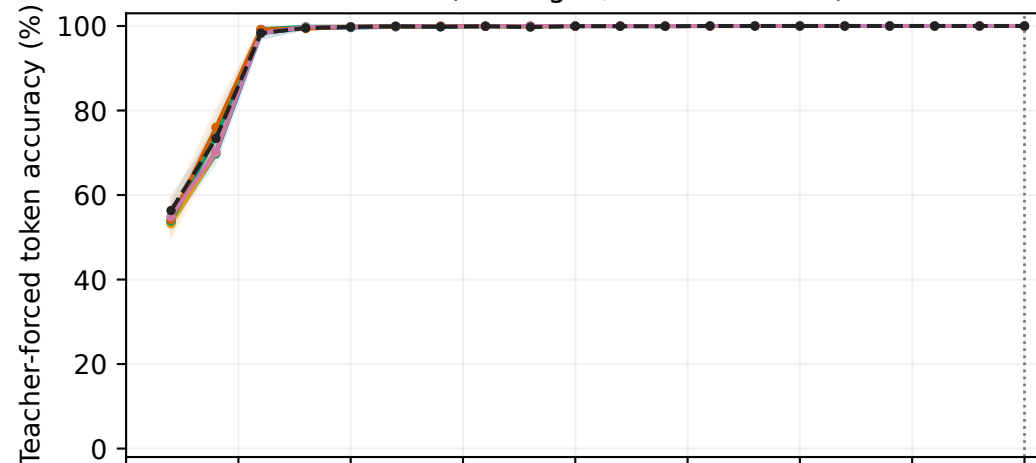
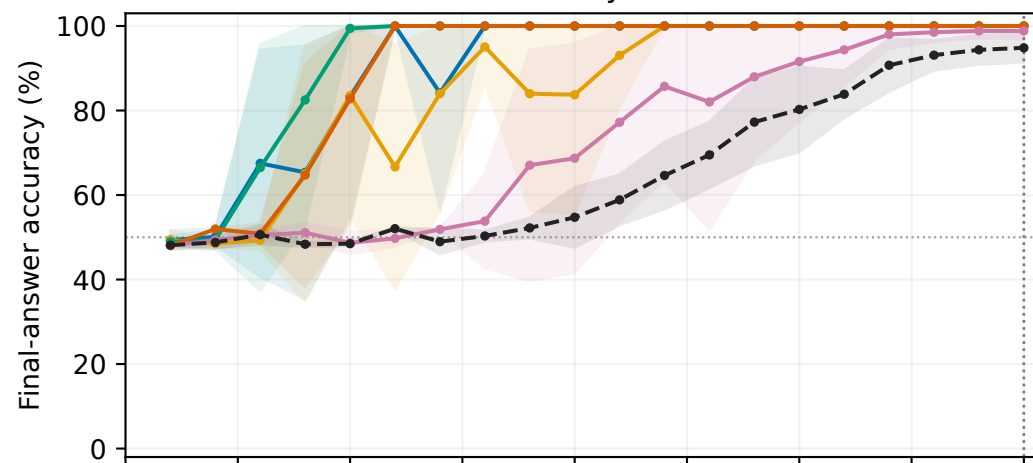


RLT-1 4+4 RLT-1 6+2 RLT-1 8+0
 RLT-1 5+3 RLT-1 7+1 Transformer 8

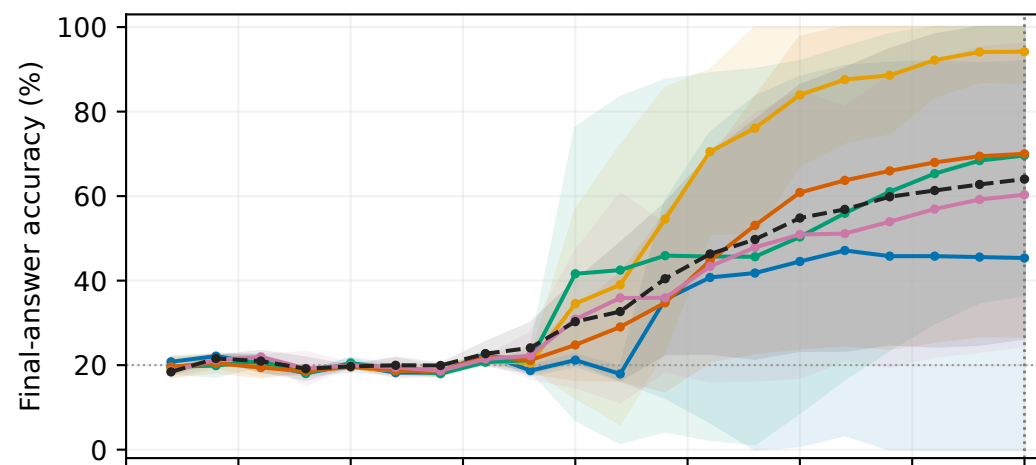
Addition (1-8 digits; teacher forced)



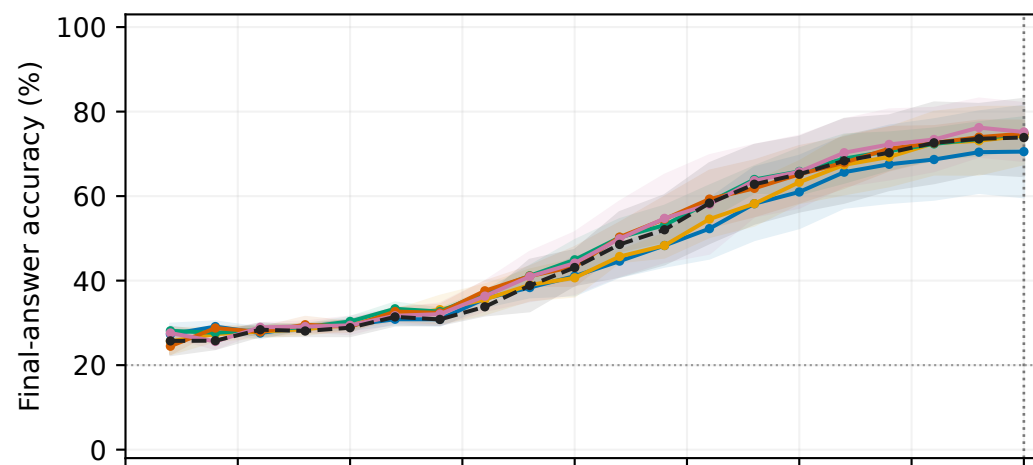
Parity



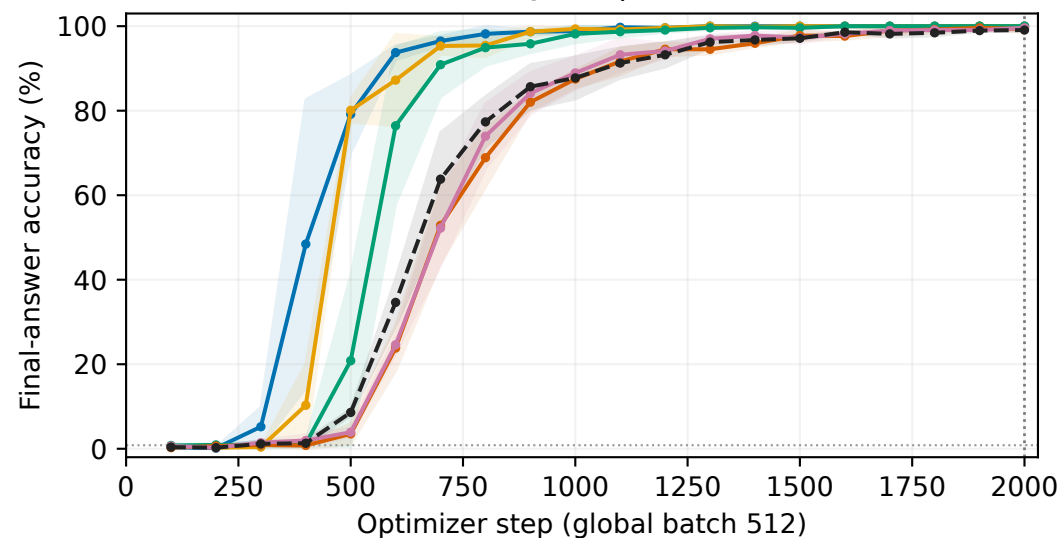
Mod 5: no brackets



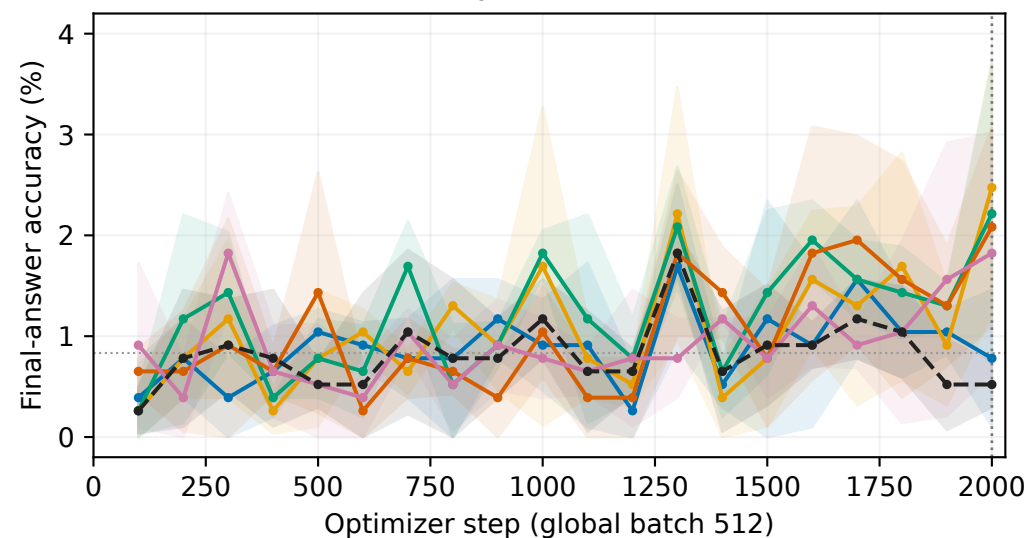
Mod 5: with brackets



S_5 : swaps



S_5 : standard



All tasks: seeds 42, 43, 44 · mean ± sample SD